

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Canceled)
2. (Currently amended):     ~~The A~~ signal processing circuit ~~of claim 1~~, comprising  
          an attenuating unit that attenuates a demodulated signal acquired by detecting a received  
signal;  
          a detecting unit that detects a first signal indicating intensity of the demodulated signal;  
          an attenuation rate setting unit that sets an attenuation rate of the attenuating unit,  
          depending on the first signal and a second signal indicating a change in an  
envelope curve acquired based on an intermediate frequency signal of the received signal, or  
          depending on the first signal, the second signal, and a third signal indicating  
electric field intensity acquired based on the intermediate frequency signal; and  
          a difference determining unit that determines whether a predetermined threshold is  
exceeded by a difference ~~between of intensity of the demodulated signal and a signal obtained by~~  
smoothing the demodulated signal, having a predetermined time difference to detect noise,  
wherein the attenuation rate setting unit  
  sets a first attenuation rate for the attenuating unit based on the detection  
of noise by the difference determining unit, and  
  after a predetermined period has elapsed from the setting, sets a second  
attenuation rate smaller than the first attenuation rate for the attenuating unit.
3. (Currently amended):     ~~The A~~ signal processing circuit ~~of claim 2~~, comprising:

an attenuating unit that attenuates a demodulated signal acquired by detecting a received signal;

a detecting unit that detects a first signal indicating intensity of the demodulated signal;

an attenuation rate setting unit that sets an attenuation rate of the attenuating unit,

depending on the first signal and a second signal indicating a change in an envelope curve acquired based on an intermediate frequency signal of the received signal, or

depending on the first signal, the second signal, and a third signal indicating electric field intensity acquired based on the intermediate frequency signal;

a difference determining unit that determines whether a predetermined threshold is exceeded by a difference between the demodulated signal and a signal obtained by smoothing the demodulated signal, to detect noise; and

a difference threshold setting unit that sets the threshold in the difference determining unit based on at least one signal of the first signal, the second signal, and the third signal, wherein

the attenuation rate setting unit sets a first attenuation rate for the attenuating unit based on the detection of noise by the difference determining unit, and after a predetermined period has elapsed from the setting, sets a second attenuation rate smaller than the first attenuation rate for the attenuating unit.

4. (Currently amended): ~~The A~~ A signal processing circuit of claim 2, ~~wherein the signal processing circuit comprises~~ comprising:

a smoothing processing unit that smoothes the a demodulated signal acquired by detecting a received signal with frequency characteristics of a de-emphasis process reducing noise of the received signal; and wherein the difference determining unit determines whether the predetermined threshold is exceeded by a difference of intensity of the demodulated signal having the predetermined time difference, which is smoothed by the smoothing processing unit, and wherein

the an attenuating unit that attenuates the demodulated signal smoothed by the smoothing

processing unit;

a detecting unit that detects a first signal indicating intensity of the demodulated signal;  
an attenuation rate setting unit that sets an attenuation rate of the attenuating unit,  
depending on the first signal and a second signal indicating a change in an  
envelope curve acquired based on an intermediate frequency signal of the received signal, or  
depending on the first signal, the second signal, and a third signal indicating  
electric field intensity acquired based on the intermediate frequency signal; and  
a difference determining unit that determines whether a predetermined threshold is  
exceeded by a difference between the demodulated signal and a signal obtained by smoothing the  
demodulated signal, to detect noise, wherein  
the attenuation rate setting unit sets a first attenuation rate for the attenuating unit based  
on the detection of noise by the difference determining unit, and after a predetermined period has  
elapsed from the setting, set a second attenuation rate smaller than the first attenuation rate for  
the attenuating unit.

5. (Currently amended): The signal processing circuit of claim 2 4, comprising:

a noise detecting unit that outputs a noise detection signal based on the second signal and the third signal; and

an output controlling unit that inputs the output of the attenuating unit and the demodulated signal, ~~and sets to add and output while setting~~ amplification rates of the output of the attenuating unit and the demodulated signal based on the noise detection signal, to add and output the output of the attenuating unit and the demodulated signal amplified with the set amplification rates.

6. (Original): The signal processing circuit of claim 5, wherein the output controlling unit comprises:

a first amplifying unit that sets a level of the demodulated signal;

a second amplifying unit that sets a level of the output of the attenuating unit; and  
an adding unit that adds and outputs the output of the first amplifying unit and the  
output of the second amplifying unit, and wherein

the output controlling unit increases the amplification rate of the second amplifying  
unit and performs adding and outputting, if the noise detection signal indicates that noise is  
detected, and wherein

the output controlling unit increases the amplification rate of the first amplifying unit  
and performs adding and outputting, if the noise detection signal does not indicate that noise is  
detected.

7. (Currently amended): The signal processing circuit of claim 2 ~~1~~, wherein the received  
signal is an FM received signal.